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In re the Application of

**Deng et al.**Docket Number: **TI-32801**Serial No.: **10/039,484**Art Unit: **2826**Filed: **12/31/2001**Examiner: **J. Mondt**

For: **METHOD AND APPARATUS FOR DETERMINING PARASITIC  
CAPACITANCES IN AN INTEGRATED CIRCUIT**

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<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
NAME OF INVENTOR(S): <b>Deng et al.</b>	
TITLE OF INVENTION: <b>Method and Apparatus for Determining...</b>	
TI FILE NO.: <b>TI-32801</b>	DEPOSIT ACCT NO.: <b>20-0668</b>
FAXED: <b>09/17/2003</b> DUE: <b>10/11/2003</b> ATTY/SECY: <b>YT/jsm</b>	
RECEIPT DATE & SERIAL NO.: Serial No.: <b>10/039,484</b> Filing Date: <b>12/31/2001</b>	

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Texas Instruments Incorporated  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION**

Appl. No. : 10/039,484  
Applicant : Xiaowei Deng  
Filed : December 31, 2001  
TC/A.U. : 2826  
Examiner : Johannes P. Mondt

Confirmation No. 9755

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Docket No. : TI-32801  
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9-17-03

*Jackie McBride*  
Jackie McBride

**ELECTION**

Sir:

In response to the Examiner's restriction requirement mailed September 11, 2003, Applicants hereby elect to pursue Group I, claims 1-46, for examination purposes.

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Respectfully submitted,

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